

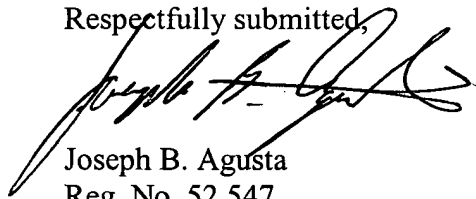
Remarks

This preliminary amendment is being filed in the present application which is a continuation of application Serial Number 09/598,558 filed on June 21, 2000.

Claims 1-13 have been cancelled without prejudice. New claims 14-29 have been added. Claims 14-29 are presently pending.

New claims 14-29 have been added to cover certain aspects of the present invention relating to context switching between software tasks. Referring to Fig. 3 and the text at page 10, line 11 et seq., column 304 represents the physical configuration of the array organized in the core processor, while column 312 represents the operation of the array. Depending on the context status bit (CSB) stored in the processor state register, the operation of the array may operate as its defined physical configuration as shown in rows 320 and 322, for example, or may operate as if the array had a different physical configuration as shown in columns 324 and 326, for example.

Respectfully submitted,



Joseph B. Agusta
Reg. No. 52,547
5015 Southpark Drive, Suite 230
Durham, NC 27713
(919) 806-1600